**Campus Hiring Opennings in NXP Suzhou**

**About the company:**

NXP Semiconductors N.V. (NASDAQ: NXPI) enables secure connections and infrastructure for a smarter world, advancing solutions that make lives easier, better and safer. As the world leader in secure connectivity solutions for embedded applications, NXP is driving innovation in the secure connected vehicle, end-to-end security & privacy and smart connected solutions markets. Built on more than 60 years of combined experience and expertise, the company has 45,000 employees in more than 35 countries.

Interested candidates please send your updated resume to [emily.qian@nxp.com](mailto:emily.qian@nxp.com), specifying the title of the position you are applying for in the subject.

**Linux Driver Development Engineer**

**Responsibilities:**

* Develop / maintain Linux BSP drivers with quality for i.MX portfolio on Freescale reference boards to support Linux and Android.
* Profile / optimize system and driver performance.
* Analyze, investigate and resolve system level issues together with other teams.
* Review SoC reference manual, write driver design document and Linux BSP reference manual.
* Actively support our key customers.
* Linux BSP code upstream.

**Requirements:**

* Bachelor or master degree in electrical or electronic engineering, computer science or relevant disciplines.
* Familiar with assembly or C/C++ programming and embedded OS like Linux or RTOS.
* Extensive embedded software development experience including device drivers and consumer embedded applications.
* Deeply understanding of Linux device driver model and Linux kernel.
* Experience in one of following area is preferred:    
  • The real experience for the device driver development for Linux.   
  • Understand audio drivers  
  • Graphics, image processing  
  • Storage and connectivity, such as USB, SD, PCIe, SATA, etc.  
  • Display, video, camera  
  • V4L, FB, X-server, DirectFB  
  • Experience with MIPI, HDMI interface
* Experience with ARM architecture is preferred.
* Having Linux kernel patch up-streamed is preferred.

**Android Development Engineer**

**Responsibilities:**

* Develop / maintain Android BSP drivers with quality for i.MX portfolio on Freescale reference boards to support Linux and Android.
* Profile / optimize system performance.
* Analyze, investigate and resolve system level issues together with other teams.
* Review SoC reference manual, write driver design document and Android BSP reference manual.
* Actively support our key customers.
* Android BSP code upstream.

**Requirements:**

* Bachelor or master degree in electrical or electronic engineering, computer science or relevant disciplines.
* Familiar with assembly or C/C++ programming and embedded OS like Linux or RTOS.
* Extensive embedded software development experience including device drivers and consumer embedded applications.
* Understand Android framework.
* Experience in one of following area is required or strongly preferred:    
  • The real experience for the device driver development for Android.   
  • Rendering/display subsystem.  
  • Connectivity including Wi-Fi, Ethernet, 3G/4G, SDIO, etc.  
  • Experience with MIPI, HDMI interface.
* Experience with ARM architecture is preferred.
* Having Android patch up-streamed is preferred.

**MCU System Engineer**

**Responsibilities:**

* Design and develop digital circuits for ARM core based SOC projects and IPs.
* Verification in module level and chip level, define and execute verification plan with full functional coverage.
* Involved in the SoC level and Digital IP design and verification, and the SoC development for ARM based SoC projects.
* Do RTL coding, integration and verification.
* Do simulation in gate level, transistor level (full-chip spice).
* Create function test patterns for testing engineering.

**Requirements:**

* Bachelor or master degree in Microelectronics, Electronics, Electrical Engineering, Computer Science or relevant disciplines.
* Good knowledge and have experience in digital circuit design/verification with Verilog/VHDL.
* Can use the EDA tools from Cadence, Synopsys, or Mentor tools for digital and/or analog developing.
* Have knowledge about computer architecture, 8bit, 16bit or 32bit Micro-controller or Micro-processer is a plus.
* Have knowledge on AMBA bus system is a plus.
* Knowledge and experiences on verification and verification methodology is a plus.
* Good language skill in English. Passed CET-6.
* Have basic knowledge of VLSI design flow.

**MCU Software Development Engineer**

**Responsibilities:**

* Develop / port / maintain core MCU software (bare metal software, RTOS, driver, stack and boot loader, etc.) with quality to support Microcontroller portfolios.
* Profile / optimize the code size / performance of the software.
* Analyze, investigate and resolve system level issues.
* Write design document and MCU software reference manual.
* Build expertise on specific application areas.
* Actively support key customers and mass market customers.

**Requirements:**

* Bachelor or master degree in electrical or electronic engineering, computer science or relevant disciplines.
* Good understanding on general MCU architecture and peripherals.
* Solid knowledge on assembly or C/C++ programming and embedded system.
* Good understanding on MCU SW tool chain / compiler / debugger.
* Experience in one of following areas is preferred: RTOS kernel development, driver development, and stack development.
* Familiarity with RTOS is a plus.
* Experience with ARM based MCU is a plus.

**Design Validation Engineer**

**Responsibilities:**

* Build FPGA prototyping system, and setup FPGA simulation environment
* Do RTL changing work during porting ASIC design to FPGA platform.
* Do Verification in module level and chip level.
* Define the validation plan, create and execute validation test both on the pre-silicon and post-silicon validation platform
* Responsible for finding out the design/doc issues, and providing the workaround

**Requirements:**

* MS in Communications, Microelectronics or Computer engineering
* Familiar with Xilinx device structure & synplicity tools (synplify\_premier, identify, certify) & Xilinx ISE/PAR tools
* Familiar with Verilog HDL
* Familiar with software development, and on-board debugging;
* Strong background in C
* Big plus with

• Experience in IAR and CodeWarrior debugger Tool

• Logic simulators and debug tools (VCS, NCSIM, Verdi and etc.)

• With experience in DDR, LPDDR2, DDR2, USB 2.0 or Ethernet

* Experience in ARM M0+, M4 and M7 based MCU
* Fluent English (both written and spoken) and excellent communication skills. Ability to write professional and technical reports and procedures.