题目: Concept and update of digital video coding research

时间: 2011年12月28日星期三下午2:30

地点:信电系 215 会议室 报告人: Dr. Huifang Sun

Fellow, Mitsubishi Electric Research Laboratories, IEEE Fellow



Abstract

Digital video signal processing is an area of science and engineering that has developed rapidly over the past decade. The video coding standards developed by the moving picture expert group (MPEG) and ITU Video Coding Experts Group (VCEG) play a very important role for the video industry, which provide key technologies for digital transmission and storage of video signals. In this talk, I will briefly present the concept of digital video coding and give an update of recent video coding standard activities and progresses on the HEVC. And 3D Video.

Short Bio of Speaker

Huifang Sun graduated from Harbin Military Engineering Institute, Harbin, China, and received the Ph.D. from University of Ottawa, Canada. He was an Associate Professor in Fairleigh Dickinson University in 1990. He joined to Sarnoff Corporation in 1990 as a member of technical staff and was promoted to a Technology Leader later. In 1995, he joined Mitsubishi Electric Research Laboratories (MERL) and was promoted as Vice President, Deputy Director and Fellow in 2003 and currently is a Fellow of MERL. He has co-authored two books and published more than 140 Journal and Conference papers. He holds more than 60 US patents. He received Technical Achievement Award for optimization and specification of the Grand Alliance HDTV video compression algorithm in 1994 at Sarnoff Lab. He received the best paper award of 1992 IEEE Transaction on Consumer Electronics, the best paper award of 1996 ICCE and the best paper award of 2003 IEEE Transaction on CSVT. He was an Associate Editor for IEEE Transaction on Circuits and Systems for Video Technology and was the Chair of Visual Processing Technical Committee of IEEE Circuits and System Society. He is an IEEE Fellow.