

## OmniVision 豪威科技 2017 校园招聘

学校 宣讲时间 宣讲地点  
浙江大学 2016-10-22 14:00 玉泉永谦活动中心第一报告厅

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公司网址: [www.ovt.com](http://www.ovt.com)

美国豪威科技股份有限公司 (OmniVision Technologies, Inc., 豪威科技) 是一家跨国高科技公司, 致力于设计、研发和销售 CMOS 影像传感器, 是影像系统解决方案的创新技术领导者。公司 1995 年创建于美国硅谷, 产品基于独特的 CameraChip、CameraCubeChip 技术和高度整合的单芯片 CMOS 摄像解决方案, 具有尺寸小、重量轻、耗电低、可靠性高、容易集成等特点, 市场覆盖广泛, 形成了体系完整的 CMOS 影像传感器家族。豪威科技注重技术研发与创新, 目前在全球拥有 3400 多项专利和专利申请, 产品在许多应用市场皆处于技术领先地位。

### 产品应用

豪威科技持续创新, 技术领先。公司在充分考虑客户需求的基础上, 高效开发自身产品, 并以专业的成像技术背景, 成功有效地启发、协助客户的产品研发。在强大、专业的研发团队与全球供应链合作伙伴、客户的紧密合作下, 豪威科技能在面对不同终端应用时, 提供丰富而完善的 CMOS 摄像解决方案, 即能满足包括平板电脑、笔记本电脑、安防和监视系统、移动电话、车载应用、医疗生物器械在内的传统市场, 也适用于 AR、VR、和无人机的计算机视觉等应用新兴市场。豪威科技以技术为依托, 精益求精为客户提供支持和服务, 客户选择豪威, 产品成功将事半功倍。

### 全球战略

豪威科技坐落在美国加州具有高科技摇篮之称的硅谷。公司办公设施现代, 总部占地约为 23,000 平方米, 并在全球多地地设立有分部, 通过遍布世界、经验丰富的工程和技术团队为全球客户提供便捷、优质、专业和高效的服务。公司在中国布局有两个研发中心: 坐落于上海张江高科技园区内的豪威科技(上海)有限公司和位于有光谷之称的武汉东湖高新技术开发区的豪威科技(武汉)有限公司。前者主要致力于 CMOS 摄像芯片设计、应用开发的工程技术及市场拓展; 后者则侧重于软件开发、系统集成、以及图像、流媒体应用和提高的研究, 同时从事大型图像集成、流媒体的分享、计算、有关云存储处理、云计算和云运用等开发。

此外, 豪威科技还在上海松江出口加工区设立了技术装备均最为先进的加工检测中心豪威半导体(上海)有限责任公司, 以及微型镜头及感应芯片的模组研发生产中心豪威光电子科技(上海)有限公司。

**基本要求:** 2017 届硕士及以上学历应届毕业生。

**简历投递:** 请以“学校全称+姓名+应聘职位+工作地点”为邮件标题, 投递简历到公司校园招聘邮箱: [campus@ovt.com](mailto:campus@ovt.com) 也可以于宣讲会现场提交

**招聘流程:** 投递简历到邮箱/宣讲会现场提交->简历评阅->电话初选/面试->复试->offer

**温馨提示:** 请各位同学务必保持所留电话的畅通, 以免错过面试机会

**职位总览:**

工作地点: 上海

- (1) ASIC Design Engineer ASIC 设计工程师
- (2) ASIC Design Engineer – Wireless ASIC 设计工程师-无线通信
- (3) Digital Design Engineer 数字电路设计工程师
- (4) System Design Engineer 系统设计工程师
- (5) Analog Design Engineer 模拟电路设计工程师
- (6) Algorithm Development Engineer 图像算法研发工程师
- (7) Embedded System Engineer 嵌入式系统工程师
- (8) Embedded Software Engineer-Multimedia SoC 嵌入式软件工程师-多媒体
- (9) System Engineer 系统工程师
- (10) RFIC Test Engineer RFIC 测试工程师

工作地点: 武汉

- (11) Image Quality Engineer 图像质量工程师
- (12) Algorithm Development Engineer 图像算法研发工程师
- (13) Software Developing Engineer – Cloud Computing 软件开发工程师-云计算
- (14) Software Developing Engineer – Mobile 软件开发工程师 – 移动终端开发
- (15) Software Developing Engineer – Optimization 软件开发工程师-算法优化
- (16) Software Developing Engineer – Web 软件开发工程师-云服务及网站开发
- (17) Software Developing Engineer – Embedded 软件开发工程师-嵌入式开发
- (18) Software QA Engineer 软件测试工程师

**详细信息:**

**工作地点: 上海**

**(1) ASIC Design Engineer ASIC 设计工程师**

Position Overview:

The candidate will join a team of highly competent ASIC designers involved in design, verification, and implementation (ASIC) of advanced platform for OmniVision's future generation multi-media products.

Responsibilities:

1. Provide detailed block-level design and documents;
2. Develop and execute thorough block level simulation and lab verification plan;
3. Participate in the FPGA platform development and lab debugging;
4. Participate in block level architecture design;
5. Assisting embedded FW development.

Requirements:

MSEE/BSEE;

Strong analytical, and problem solving skills as well as hands-on lab debugging skills;

Good knowledge of RTL design and simulation;

Able to write C code to model RTL blocks for simulation and verification;

Able to write reusable Verilog RTL codes, follow design and DFT guidelines;

Able to run synthesis, static timing analysis and formal verification is highly desirable, but not required;

Knowledge in languages relevant to the ASIC development process including Verilog, System Verilog, Unix Scripting, Perl and Tcl is strong plus;

Knowledge of Video/Peripheral/SOC is a plus;  
Good communication skills, especially in technical writing and reporting;  
Self-motivated and ability to excel in a team environment.

## **(2) ASIC Design Engineer – Wireless    ASIC 设计工程师–无线通信**

### Responsibilities:

The candidate will be involved in design, verification, and implementation of advanced platform for OmniVision's future generation wireless products. He/She will responsible for RTL design, SOC integration and FPGA system evaluation of wireless system.

### Responsibilities:

1. Be responsible for RTL coding, simulation and implementation;
2. wireless communication protocol research, performance evaluation, design optimization;
3. Working with project team members to contribute Bluetooth & wlan SOC design to product;
4. Provide detailed block-level implementation and documentation;
5. Develop and execute thorough simulation and lab verification plan;
6. Participate in the FPGA platform development and lab debugging;
7. Assisting embedded FW development.

### Requirements:

1. MSEE or PhD.
2. Familiar with verilog;
3. Familiar with communication system, design implementation;
4. Familiar with 802.11 PHY/Mac protocol is a big plus;
5. Good knowledge of communication theory is a big plus.
6. Strong analytical, and problem solving skills as well as hands-on lab debugging skills;
7. Good communication skills, especially in technical writing and reporting;
8. Self-motivated and ability to excel in a team environment

## **(3) Digital Design Engineer    数字电路设计工程师**

### Position Overview:

Digital design and verification for multi-media SoC products

### Responsibilities:

The candidate will be involved in digital design, verification, and implementation of advanced platform for OmniVision's future generation multi-media products. He/she will be responsible to provide detailed block-level or system-level design and documents, develop and execute simulation and lab verification plan, and also participate in the FPGA platform development and lab debugging.

### Requirements:

Technical Abilities

1. Strong analytical, and problem solving skills as well as hands-on lab debugging skills;
2. Good knowledge of RTL design and simulation;
3. Able to write C/C++ code to model RTL blocks for simulation and verification;
4. Able to write reusable Verilog/System Verilog RTL codes follow design and DFT guidelines.
5. Able to run synthesis, static timing analysis and formal verification is highly desirable, but not required;
6. Knowledge in languages relevant to the ASIC development process including Verilog, Unix Scripting, Perl and Tcl is strong plus;
7. DSP function implementation experience is a plus, but not required;
8. Self-motivated and ability to excel in a team environment.

Communication skills: Good English communication skills, especially in technical writing and reporting

#### **(4) System Design Engineer 系统设计工程师**

##### Position Overview:

As a System Design Engineer, you will work with IC design engineers to help with IC verification and chip validation form RTL to IC debugging, as well as system reference design for IC products and chip production testing support.

##### Responsibilities:

Support ASIC design team for FPGA verification and chip validation;  
ASIC project FPGA verification environment preparation and improve;  
Design & evaluate schematics of boards which FPGA verification, chip validation and system projects needed;  
Develop / Improve reference design for SOC chip / system requirement for SOC chip;  
Provide hardware environment for chip verification;  
Assisting embedded FW development;

##### Requirements:

MSEE/ BSEE

Basic skill of FPGA and system design are required;

A thorough knowledge of electronic circuits and systems with practical experience in digital circuit design and debug;

Knowledge in operating lab equipments, such as logic scope, and conducting lab tests with analysis equipments;

Able to write reusable Verilog RTL codes,

Knowledge in embedded software with C writing and PCB layout is a great plus.

Knowledge in languages relevant to the system development process including Verilog, Unix Scripting and Perl is also desirable;

Good communication skills, especially in technical writing and reporting;

Self-motivated and ability to excel in a team environment.

## **(5) Analog Design Engineer 模拟电路设计工程师**

### Position Overview:

As an Analog IC Design Engineer you will perform analog and mixed signal design, characterization and evaluation of analog circuitry for PMU's, ADC/DAC's, and other base band analog circuits

### Responsibilities:

Design and R&D analog circuit, PMU, AD/DA, and other base band analog circuit.

Use EDA tool to run simulation and function verification.

Guide layout engineer to optimize layout

Chip debug and testing individually

Design and optimize chip layout

### Requirements:

1. MSEE in analog IC design;
2. Experience in Cadence EDA tools;
3. Team player with good communication skills
4. Speciality in EE and Microelectronic

## **(6) Algorithm Development Engineer 图像算法研发工程师**

### Position Overview:

The candidate will be responsible to research and development of image processing and pattern recognition algorithms.

### Responsibilities:

Research and develop still/motion image processing algorithms based on our sensor or application;

research and develop the corresponding algorithms of pattern recognition;

algorithm simplification/optimization in mathematics or hardware;

Algorithm testing and verification;

Algorithm integration in system architecture

### Requirements:

1. Experience in still/motion image processing;
2. Strong mathematics background, and algorithm designing ability;
3. Excellent programming skills in C/C++ and matlab;
4. Experience in algorithm optimization including software and hardware
5. Excellent verbal and written communication skills.

## **(7) Embedded System Engineer 嵌入式系统工程师**

### Position Overview:

## Embedded Software Engineer for Catalog SoC products

### Responsibilities:

The candidate will be responsible for developing key technologies to build a complete platform solution from bottom to top that covers various application frameworks including Android/iOS and Linux etc. And he/she will also be responsible for integrating key components together, optimizing the performance and power for the whole platform to build a turnkey solution that includes various products such as Smartphone, IPCamera, etc. In other words, the candidate will face the challenges to integrate all necessary building blocks, develop new technologies for emergent usage scenarios and go through whole product life cycle to create a real product.

### Requirements:

#### Competencies Required

1. R&D design experience on embedded processors (ARM) and embedded OS
2. Good troubleshooting capability
3. Driver for Results
4. Sense of collaboration and teamwork, rewards and celebrates team successes and achievements
5. Aggressive, initiative, independent, self-motivated and hard working with good interpersonal skill
6. Smart and fast learning
7. Communication skills: Efficient English communication, presentation skill

#### Technical Abilities

1. Video Surveillance, Consumer Electronics, Industrial, System Knowledge
2. Familiar with H/W & S/W architecture, have work experience on ARM or Digital Signal Processing, Image & Video processing is preferred.
3. Familiar with Embedded OS
4. C/C++/ASM programming
5. Java programming is a optional.

## **(8) Embedded Software Engineer-Multimedia SoC 嵌入式软件工程师-多媒体**

### Position Overview:

This is a SoC Integration Engineer position that involves developing and verifying of our latest generation platform for multimedia applications.

### Responsibilities:

1. Develop driver/firmware on embedded platform for our own SoC
2. Work with the ASIC/hardware design team for SoC driver/firmware validation and overall system performance validation
3. Development and implementation of embedded software, including C programming and assemble

Requirements:

1. Skills in embedded system development in ARM or other MCU environment; skilled in C;
2. Any of the following skills & experience is highly desirable, but not required:
  - (a).Skills in multimedia framework development and video driver development;
  - (b). Knowledge of video compression algorithms (eg. H.264);
  - (c). Skill in solving firmware/software problems associated with media processing platforms;
  - (d). Knowledge in basic audio encoding/decoding algorithm, such as MP3 and/or AAC;
  - (e). Familiar with SoC interfaces, such as SPI, I2C, UART, SD, USB, LCD, Sensor and so on;
  - (f). Familiar with Linux kernel, or other RTOS platform.
3. Good communication skills, especially in technical writing and reporting;
4. Self-motivated and ability to excel in a team environment.

## **(9) System Engineer 系统工程师**

Position Overview:

System engineer, System and Solutions

The candidate will join a team of highly competent system designers involved in development and support of OmniVision's IoT solutions

Responsibilities:

Be responsible for development of system solutions, including firmware and applications.

Be responsible to develop testing procedure of the system solutions

Be responsible to prepare technical document of system solutions.

Support customers on solution development and debugging

Support PM for system solution evaluation and design-win

Support customers on system integration and production

Requirements:

Strong analytical, and problem solving skills as well as hands-on lab debugging skills;Familiar with firmware and/or software development.

Good knowledge on solution testing and debugging

Good knowledge on hardware system

Good communication skills, especially in technical writing and reporting;

Self-motivated and ability to excel in a team environment.

## **(10) RFIC Test Engineer RFIC 测试工程师**

Position Overview:

Chip level Test, Test equipment tool development

Responsibilities:

1. Performs RFIC block level test, verification and debug
2. Build test plan and verify the function of chip design
3. Work with FW/SW developer on system bring up

4. Work with system evaluation people for assistant on system developing and debug
5. Design test equipment tools for test automation.
6. Assistant application engineer on customer problem solving
7. Screen chips for ATE test pattern verification.

Requirements:

1. Bachelor or above degree in EE or Microelectronics area
2. Excellent learning ability
3. Familiar with Circuit design at schematic level
4. Skillful with PCB schematic and layout design
5. Knowledge of C,C++
6. Good people communication skill and team work
7. Be able to work under schedule pressure

## **工作地点：武汉**

### **(1) Image Quality Engineer 图像质量工程师**

岗位职责:

实践量化的图像质量评估;  
为不同平台及传感器提供图像质量比较报告;  
根据客户要求调试图像质量;  
与算法, 固件, 软件团队紧密合作呈现最好的图像质量;  
支持 FAE 团队解决客户问题。

任职要求:

电子、计算机和图像处理等相关专业本科及以上学历;  
熟悉数码相机系统基本概念和基础知识;  
对 CMOS 图像传感器的图像质量具有相关经验;  
具备数字图像处理和色彩学相关知识;  
熟悉相关图像算法, 如自动曝光, 自动白平衡, 自动聚焦等;  
具备图像质量评估及测量相关经验;  
具备撰写评估报告及有效沟通能力;  
具备 C 和 Matlab 相关经验;  
对技术细节有自我驱动力。

### **(2) Algorithm Development Engineer 图像算法研发工程师**

职位描述:

研究并开发图像/视频处理、计算机视觉、模式识别相关的算法。

岗位职责:

1. 开发图像/视频处理、计算机视觉、模式识别相关的新算法;
2. 提高现有算法性能, 使算法效果更好或资源消耗更少;



3. 协助其他团队进行算法在硬件/固件/软件上的移植;
4. 撰写技术文档;
5. 协助其他团队进行客户支持。

任职要求:

电子工程/计算机科学或光学等相关专业，硕士以上学历;  
熟练使用 Matlab, C++ or C 进行编程;  
具有良好的技术文档的读写能力。

### **(3) Software Developing Engineer – Cloud Computing 软件开发工程师-云计算**

职位描述:

参与设计和开发大规模多媒体、图像处理系统，需要与算法工程师、Web 开发工程师、移动应用开发工程师等协同工作。

岗位职责:

1. 设计和开发基于云架构的多媒体处理应用系统;
2. 开发流媒体协议，比如：RTSP、RTMP、HLS;
3. 将视频处理算法集成到软件系统中;
4. 通过并行处理技术优化软件系统整体性能，比如：OpenMP, OpenCL, CUDA, MPI;
5. 设计和开发在台式机和移动设备上的多媒体软件。

任职要求:

1. 计算机/电子信息工程等相关专业，硕士及以上学历;
2. 有 C/C++ 开发经验，深入理解算法、数据结构、计算机架构和网络协议;
3. 深入理解互联网技术，有大规模分布式系统开发经验;
4. 熟悉图像/视频处理、视频/音频编解码、流媒体传输协议者优先;
5. 了解公有云服务网站（Amazon、Google 等）的架构者优先。

### **(4) Software Developing Engineer – Mobile 软件开发工程师 – 移动终端开发**

职位描述:

移动终端开发工程师将专注于移动终端（包括 IOS, Android, 平板）上的图像、多媒体系统开发。

岗位职责:

1. 参与开发移动终端上的图像、多媒体应用（包括 IOS 或 Android，手机或平板）;
2. 在团队和项目组中能提供较好的技术支持;
3. 参与一定的技术研究，分析或设计工作。

任职要求:

1. 计算机或相关类专业，硕士及以上学历;
2. 有 iOS 或 Android 开发经验;
3. 有较强分析和解决问题的能力;

4. 熟悉图像处理、音视频流及相关技术者优先；
5. 有多媒体框架（例如：FFMPEG，GStreamer 等）开发经验者优先；
6. 有 ARM 处理器和 NEON 优化方面工作经验者优先。

## **(5) Software Developing Engineer – Optimization 软件开发工程师-算法优化**

### 职位描述:

算法优化软件工程师，使用多种技术优化现有的图像处理算法，如单指令多数据处理、GPU、多线程处理。

### 岗位职责:

1. 测试并分析图像算法的性能；
2. 在台式机终端的 GPU 上使用 CUDA 或 OpenCL 优化图像处理算法；
3. 在移动终端的 ARM CPU 上使用 NEON 汇编优化图像处理算法；
4. 在移动终端的 GPU 上使用 OpenCL 优化图像处理算法。

### 任职要求:

1. 电子工程/计算机科学等相关专业，硕士以上学历；
2. 理解图像/视频处理算法；
3. 熟悉数据结构、算法、计算机结构、编译器以及操作系统原理；
4. 具有使用单指令多数据流优化的相关经验，如 NEON 或 MMX；
5. 具有使用 CUDA 或 OpenCL 进行 GPU 编程的相关经验；
6. 多线程编程。

## **(6) Software Developing Engineer – Web 软件开发工程师-云服务及网站开发**

### 职位描述:

前端研发工程师主要负责开发 B/S 系统架构的前端页面展示效果，同时需要熟悉服务端相关开发、运维知识和网络通信协议，与 UI 设计工程师、服务端研发工程师协作研发系统产品。

### 岗位职责:

1. 精通 B/S 架构开发工作，了解网络通信协议；
2. 具有良好的编程风格，有一定的看代码和研究推导理解能力；
3. 精通页面布局，了解提升用户体验的常用技巧；
4. 了解互联网软件系统架构，对产品创意、设计及相关技术路线有自己的见解；
5. 能积极参与到项目设计、技术研讨中，与同事协同研发工作。

### 职位要求:

1. 计算机或相关专业，硕士及以上学历；
2. 精通 JavaScript、CSS、HTML，熟悉 jQuery、Angular 等至少一种前端开发类库或框架，至少有 1 年的实际项目经验；
3. 熟悉至少一门后台开发语言，诸如 JAVA、PHP、NodeJS 等；
4. 熟悉 HTML5，CSS3 或浏览器兼容性调试优先；
5. 熟悉优化 JavaScript 或者响应式设计开发优先；

6. 具有良好的团队协作能力和英语阅读写作能力。

## **(7) Software Developing Engineer – Embedded 软件开发工程师-嵌入式开发**

### 岗位职责:

负责从底层到上层的关键技术开发，涵盖多种应用框架包括 Android、iOS 和 Linux 等建立一个完整的平台解决方案；

负责整合关键组件，优化建立一个整体解决方案，包括各种产品，如智能手机、网络摄像机、等等。

### 职位要求:

电子工程等相关专业，硕士学历；

嵌入式处理器（ARM）和嵌入式操作系统的研发设计经验；

视频监控，消费类电子，工业和系统知识；

熟悉硬件和软件架构，有 ARM 或数字信号处理工作经验，有图像和视频处理经验者优先；

C/C++/ASM 编程；

具有 Java 编程经验，TI 系列工作经历更佳；

具有快速学习新鲜事物的能力。

## **(8) Software QA Engineer 软件测试工程师**

### 职位描述:

软件测试工程师，会参与软件系统开发的全过程。协助项目经理和开发人员改进和控制软件质量。

### 岗位职责:

1.制定和完成软件测试计划，确定软件问题及原因；

2.分析及撰写测试标准及过程；

3.维护测试文档，协助调试和修改软件；

4.分析测试结果，确保现有功能和建议正确执行；

5.分析和撰写测试标准及过程。

### 任职要求:

1.计算机科学/电子工程或其他相关专业，本科及以上学历；

2.熟悉软件测试方法论及基本方法；

3.逻辑严谨，可以独立分析和解决问题，具有团队合作意识。